

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Mario Au et al. Serial No.: 10/613,246 Group Art Unit: 2818 Examiner: To Be Assigned Confirmation No. 4741

For: INT

Filed: July 3, 2003

INTEGRATED CIRCUIT MEMORY DEVICES HAVING CLOCK SIGNAL

ARBITRATION CIRCUITS THEREIN AND METHODS OF PERFORMING CLOCK

SIGNAL ARBITRATION

Date: October 21, 2003

Mail Stop PG Pub Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

STATEMENT OF IMPROPER CERTIFICATION

The present application discloses subject matter that was originally described and illustrated in U.S. Application Serial No. 09/721,478, which issued on April 8, 2003 as U.S. Patent No. 6,546,461. Because the '478 application was also filed as a foreign application before the filing date of the present application, Applicants' request for nonpublication in the present application may have been improper. Accordingly, Applicants hereby notify the Patent Office of this potentially improper certification, which was made without any deceptive intent, and hereby reaffirm the earlier-filed request for rescission that was filed on August 13, 2003.

Respectfully submitted,

Grant J. Scott

Registration No. 36,925

USPTO Customer No. 20792

Myers Bigel Sibley & Sajovec

Post Office Box 37428

Raleigh, North Carolina 27627

Telephone: 919/854-1400 Facsimile: 919/854-1401

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop PG Pub, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on October 21, 2003.

Candi L. Riggs

Date of Signature: October 21, 2003